

TR-54D

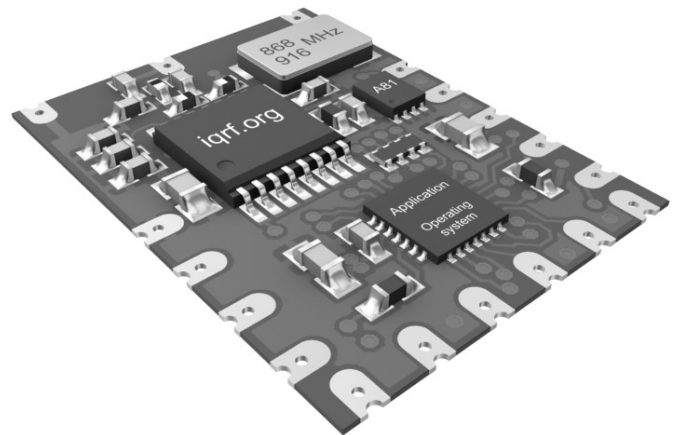
Transceiver Module

Data Sheet



Description

TR-54D is a family of IQRF transceiver modules operating in the 868 MHz and 916 MHz license free ISM (Industry, Scientific and Medical) frequency band. Its highly integrated ready-to-use design requires no external components. Extra low power consumption fits for battery powered applications. SMT mounting and very small dimensions allow space saving.



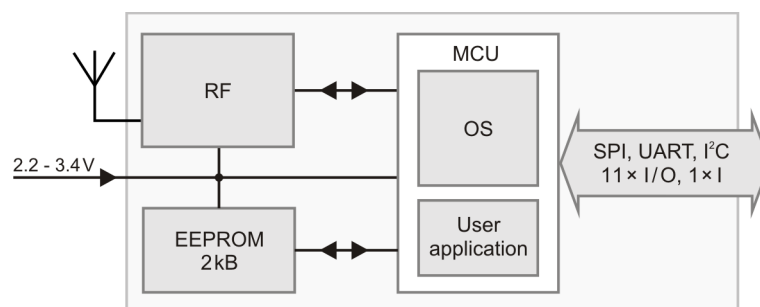
Key features

- Complete solution with operating system, easy to use
- FSK modulation
- Selectable RF band 868 / 916 MHz, multiple channel
- Selectable RF bit rate
- MCU with extended resources
- Extra low power consumption, power management modes
- SPI interface supported by OS on background
- Serial EEPROM
- Battery monitoring
- 18 pins, 11 I/Os, 1 input only, 4 analog inputs (A/D)
- Optional on-board antenna
- Stamp hole pads, SMT mounting, no SIM card compatible
- Very small dimensions

Applications

- Telemetry
- Building automation
- Control & regulation
- Remote data acquisition
- Communication links
- Wireless networks
- RF connectivity in many other areas

Block diagram



Electrical specifications
(Typical values unless otherwise stated, for brief guidance only)

Supply voltage (V_{CC}) ¹	2.2 V min., 3.4 V max., 3.0 V typ. , stabilized.
Operating temperature	0 °C to +70 °C -40 °C to +85 °C (Industrial) available on request
Supply current	
Sleep mode	380 nA (if all peripherals including MRF49XA disabled ³)
Additional supply current	800 nA (if watchdog enabled) 7.5 μ A (if brown-out detection enabled)
Run mode	1 mA (MRF49XA disabled)
Additional supply current	0.6 mA (MRF49XA on)
Rx mode	13 mA (STD mode) 400 μ A (LP mode ⁴) 35 μ A (XLP mode ⁴)
Tx mode	14 mA – 24 mA (according to RF output power)
Nominal frequency	868.35 MHz or 916.50 MHz (software selectable)
Channels	See IQRF OS User's guide, Appendix 2, Channel maps
RF data modulation	FSK (frequency-shift keying)
RF data transmission bit rate	1.2 kb/s ⁵ , 19.2 kb/s, 57.6 kb/s ⁵ , 86.2 kb/s ⁵
RF sensitivity	Depends on frequency band and bit rate:

	bit rate [kb/s]	1.2 ⁵	19.2	57.6 ⁵	86.2 ⁵
RF sensitivity [dBm]	868 MHz	-110	-104	-99	-92
	916 MHz	-109	-102	-97	-90

RF output power

Programmable in 8 levels (0 – 7), -2.5 dBm/level

level	0	1	2	3	4	5	6	7
[dBm]	-12.5	-10	-7.5	-5	-2.5	0	2.5	5

 RF range (TR-54DA)²

 Up to 850 m @ 1.2 kb/s⁵
 Up to 650 m @ 19.2 kb/s

Input voltage on Q4 to Q15 pins

 0 V to V_{CC}

A/D converter

10 bit, 4 inputs (multiplexed S&H, successive approximation)

Input A/D impedance

 10 k Ω max.

Dimensions

 20.2 mm x 14.9 mm x 3.0 mm (TR-54D)
 26.4 mm x 14.9 mm x 3.0 mm (TR-54DA)

Note 1: RF power and other parameters depend on supply voltage. Refer to datasheets of MCU and RF IC used. Test your application with respect to required supply voltage range.

Note 2: RF range strongly depends on module orientation and surroundings.

Note 3: Additional current is consumed when a peripheral is enabled.

Note 4: Depends on interferences.

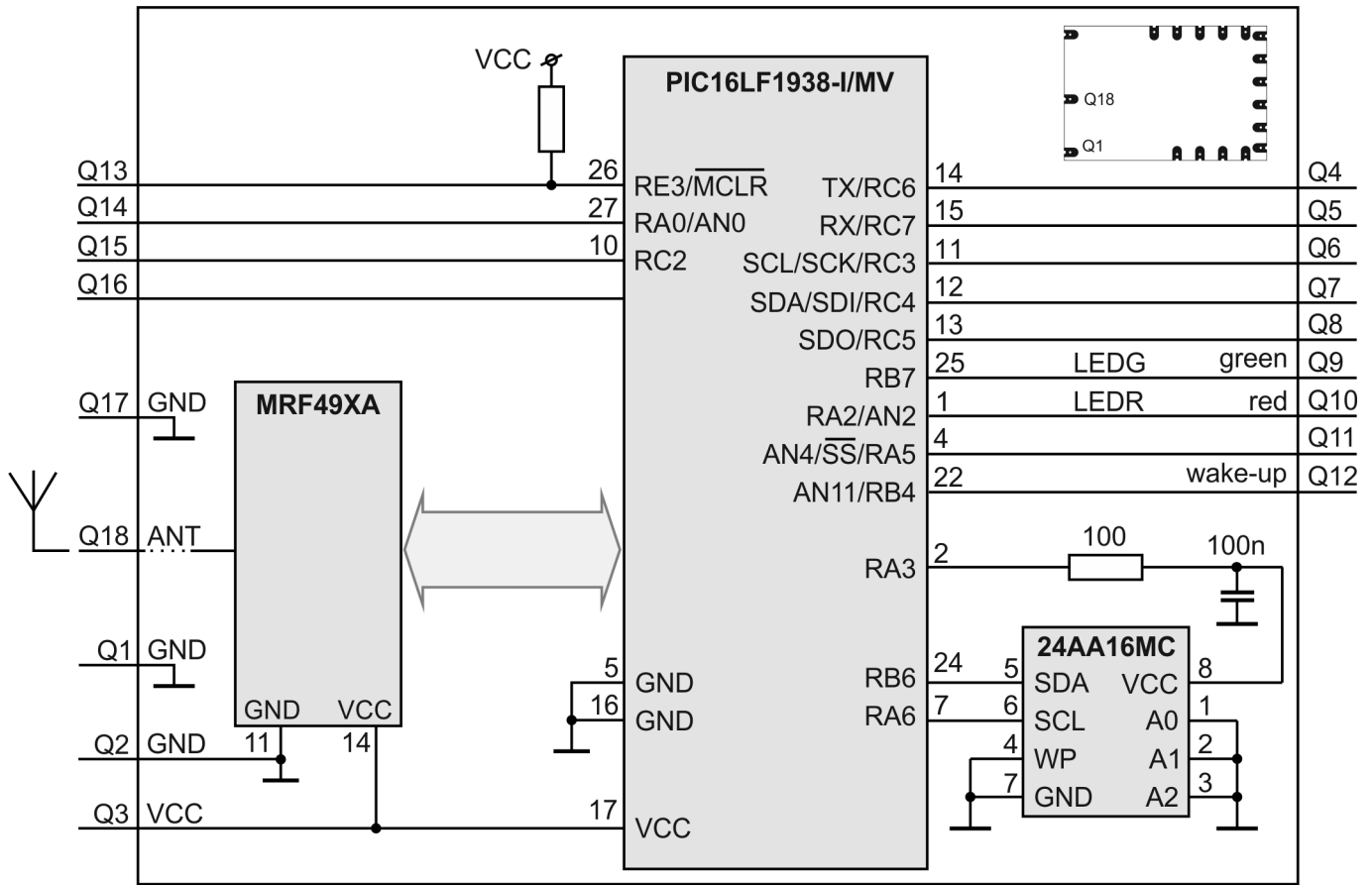
Note 5: Bit rates different from 19.2 kb/s are preliminary, for experimental purpose only.

Users have to ensure observing local provisions and restrictions relating to the use of short range devices by software, e.g. the CEPT ERC/REC 70-03 Recommendation and subsequent amendments in EU.

Absolute maximum ratings

Stresses above the following values may cause permanent damage to the device. Exposure to maximum rating conditions for extended periods may affect device reliability.

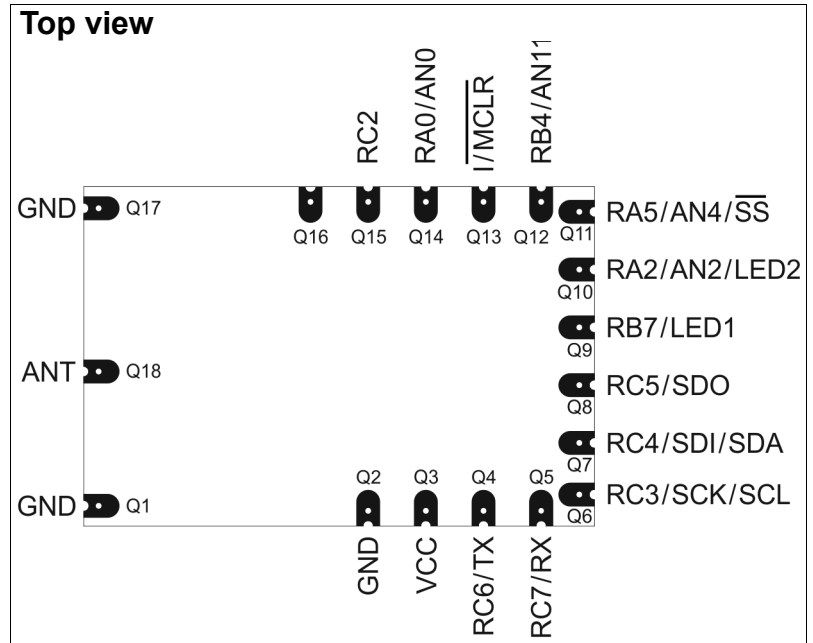
Supply voltage (V_{CC})	4 V
Voltage on Q4 to Q15 pins	-0.3 V to ($V_{CC} + 0.3$ V)
Storage temperature	-50 °C to +100 °C
Ambient temperature under bias	-40 °C to +85 °C

Simplified schematic

Basic parts

Part	Type	Manufacturer	Note
MCU	PIC16LF1938-I/MV	Microchip	
RF IC	MRF49XA	Microchip	
EEPROM	24AA16/MC	Microchip	2 kB

For more information refer to respective datasheets.

Pin	Name	Description
Q1 ⁶	GND	Ground
Q2	GND	Ground
Q3	VCC	Power supply voltage
Q4	IO/TX RC6 TX	General I/O pin UART TX
Q5	IO/RX RC7 RX	General I/O pin UART RX
Q6	IO/SCK/SCL RC3 SCK SCL	General I/O pin SPI clock input I ² C clock
Q7	IO/SDI/SDA RC4 SDI SDA	General I/O pin SPI data I ² C data
Q8 ⁷	IO/SDO RC5 SDO	General I/O pin SPI data out
Q9	IO/LED1 RB7 LED1	General I/O pin LEDR supported by OS
Q10	IO/AN/LED2 RA2 AN2 LED2	General I/O pin Analog A/D input LEDR supported by OS
Q11	IO/AN/-SS RA5 AN4 -SS	General I/O pin, Analog A/D input SPI Slave select
Q12	IO/AN RB4 AN11	General I/O pin Analog A/D input
Q13	I RE3	General input only pin
Q14	IO/AN RA0 AN0	General I/O pin Analog A/D input
Q15	IO RC2	General I/O pin
Q16		Do not use, leave unconnected
Q17 ⁶	GND	Ground
Q18 ⁶	ANT	Antenna



Note 6: Not implemented for TR-54DAx.

Note 7: This pin is used as output during initial ~250 ms boot-up to recognize programming mode.

There are no on-board protection series resistors on I/O pins. It is recommended to use 200 Ω series resistors on each pin.

Figure 1: Relative RF range vs. level for the `setTXpower(level)` function. Refer to IQRF OS Reference guide.

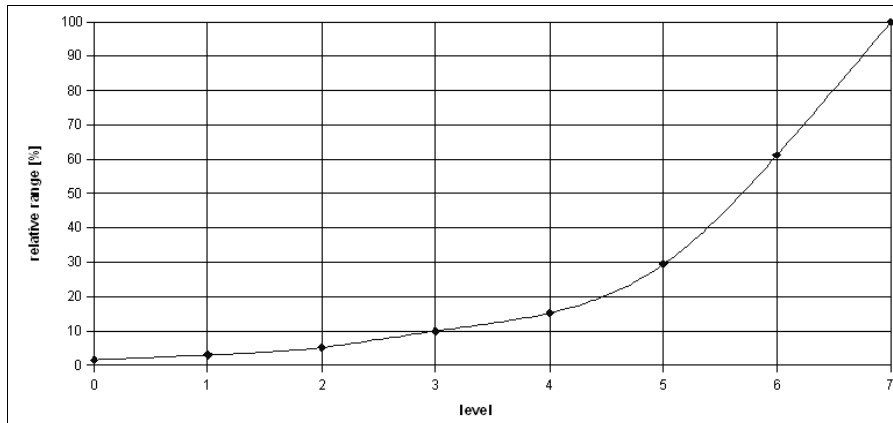


Figure 2: Relative RF range vs. level for the `checkRF(level)` function. Refer to IQRF OS Reference guide.

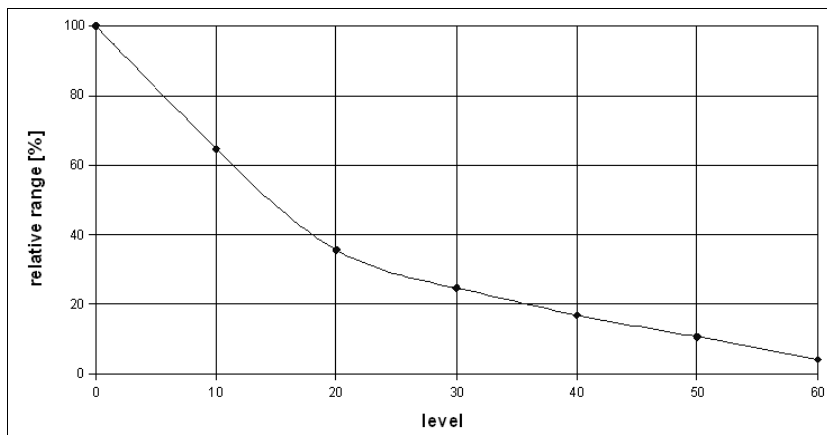
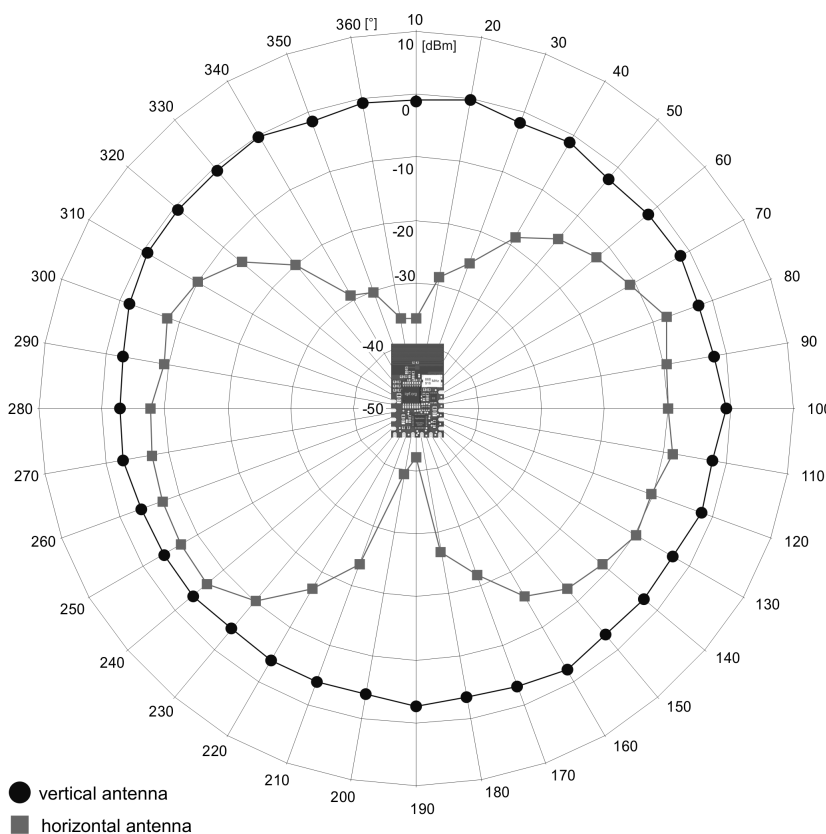


Figure 3: TR-54DA relative RF range vs. antenna orientation (radiation patterns)



Relative decrease of RF input signal vs. antenna edge spacing to conductive areas

Conductive areas close to the antenna must be avoided.

Figure 4: Perpendicular arrangement

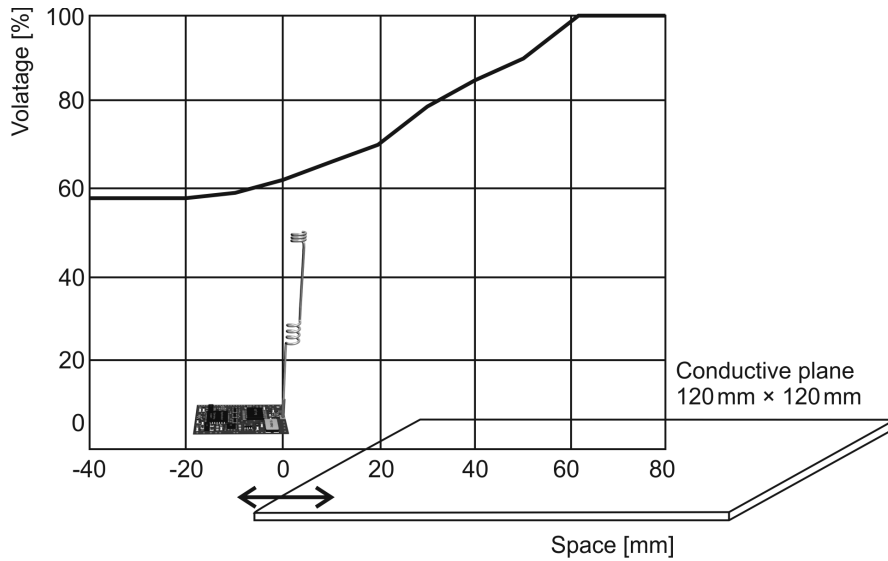
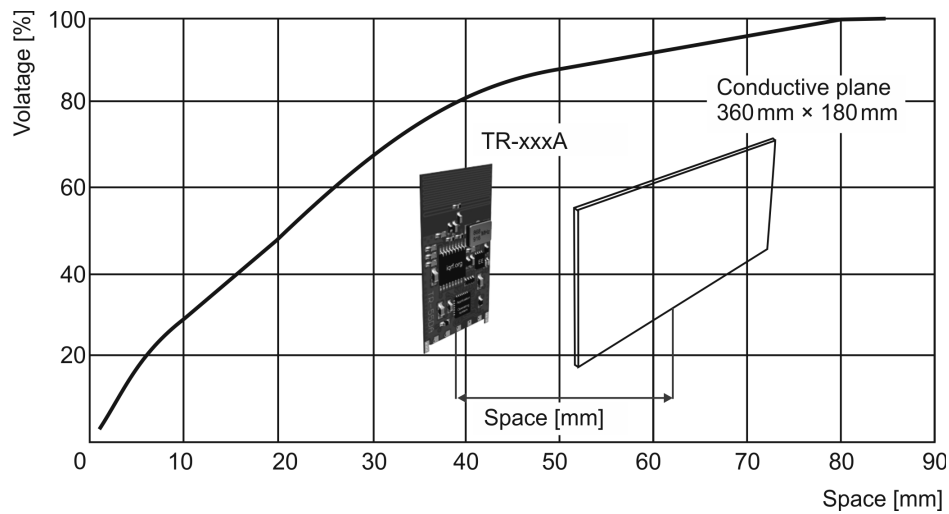
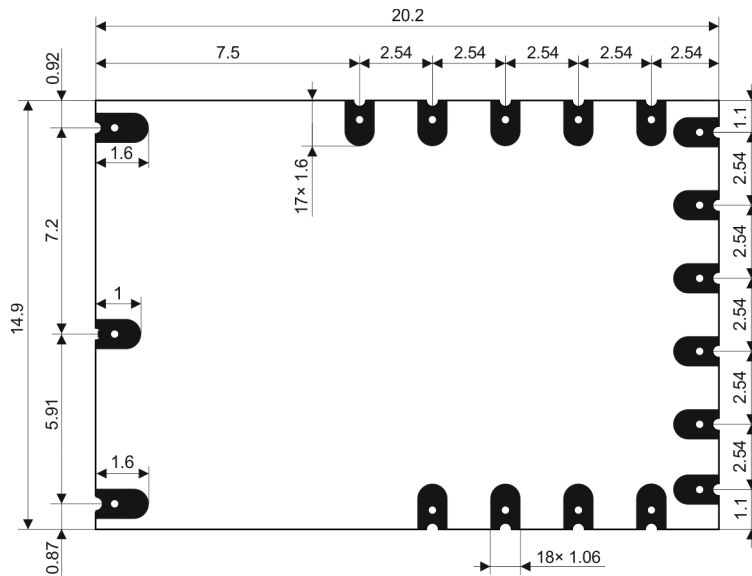


Figure 5: Parallel arrangement

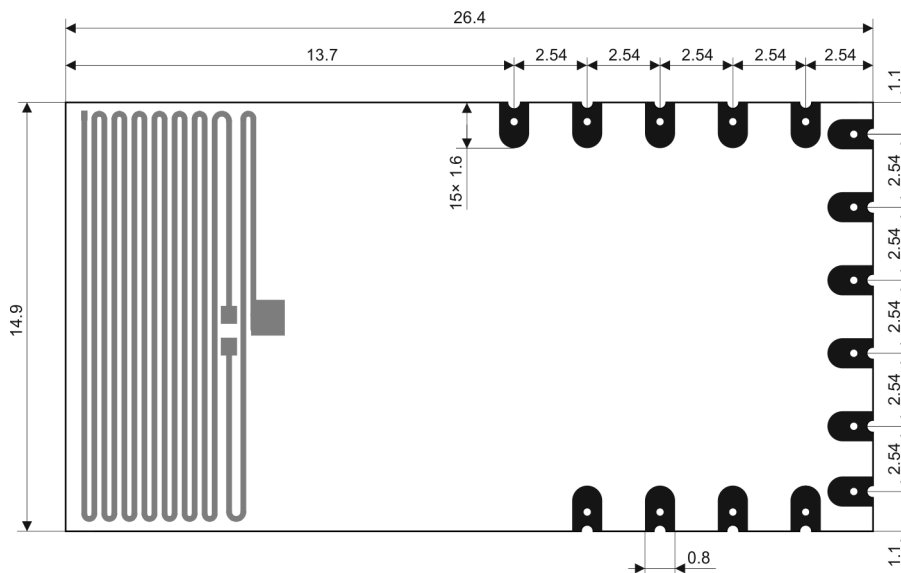


Dimensions

TR-54D

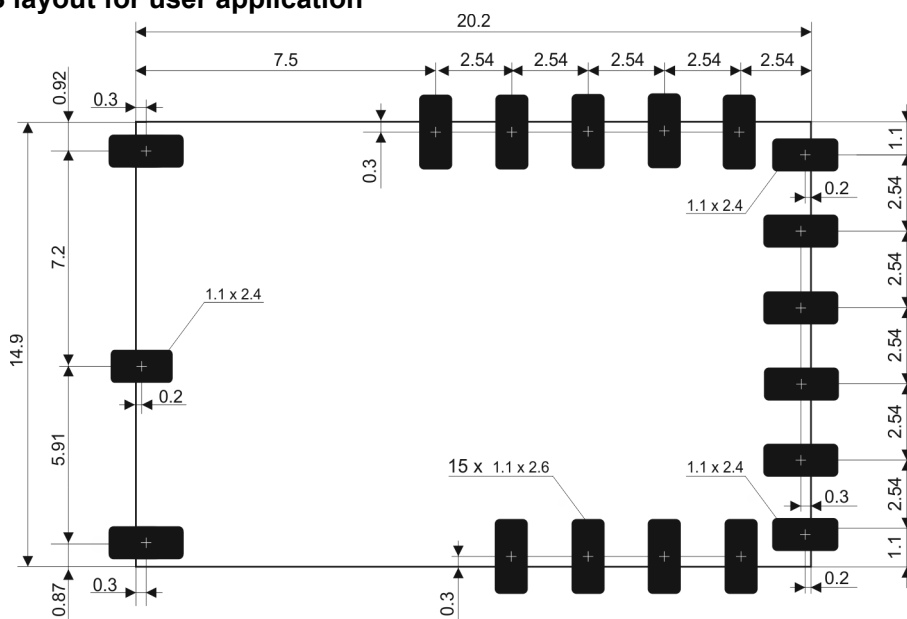


TR-54DA



Top view, units: mm

Recommended PCB layout for user application



Top view, units: mm

Sales and Service

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Complies with FCC directives FCC CFR, Title 47, Part 15, Section 15.209, FCC CFR, Title 47, Part 15, Section 15.249

Complies with Directive 2002/95/EC (RoHS)



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